IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of: Akira YOSHIDA et al.

Appl. No. 10/061,302

Confirmation No. 2370

Filed: February 4, 2002

For:

SIGNAL COMPENSATION CIRCUIT AND DEMODULATING

CIRCUIT WITH HIGH-SPEED AND LOW-SPEED FEEDBACK

LOOPS

Art Unit: 2631

Examiner:

Atty. Docket No. 31869-178004

Customer No.

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Sir:

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--SIGNAL COMPENSATION CIRCUIT AND DEMODULATING CIRCUIT WITH HIGH-SPEED AND LOW-SPEED FEEDBACK LOOPS--.

Respectfully submitted,

Date: MARCH 21, 2002

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Applicant(s)

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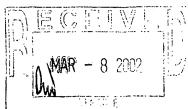
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Title

feedback

Signal compensation circuit and demodulating circuit with high-speed and low-speed-feeback-loops

Preliminary Class



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APPLICANTS Akira Yoshida, Chiba, JAPAN; Akira Horikawa, Chiba, JAPAN; Shuichi Matsumoto, Chiba, JAPAN;										
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